





# COMPONENT APPROVAL SHEET

CUSTOMER:	
CUSTOMER P/N:	
B/W P/N:	
DESCRIPTION:	BSMA1770SP-Series
DATE:	2021/2/23
REV.:	A
	客户签回栏

APPROVED	REVIEWED	PREPARED
邹爱加	管梦东	陈泉明

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# 百徽股份有限公司

						<del>炒</del> 流			丰	
		Ι	<u> </u>	图面	<u> </u>	竹 文	<u> </u>	履历	<u>衣</u>	
客	Ė	0	客户料号	0		BW料号			品名	BSMA1770SP-Series
客户 版本	君超 版本		变更内容			日期	PF	REPARED	REVIEWED	APPROVE
	A		初版		20	)21/2/23		陈泉明	管梦东	邹爱加

### **SMD Power Inductor**

**BSMA1770SP-Series** 

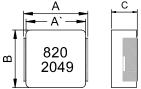
#### 1. Features

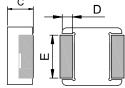
- 1. Shielded construction.
- 2. Capable of corresponding high frequency.
- 3. Low loss realized with low DCR.
- 4. High performance (Isat) realized by metal dust core.
- 5. Ultra low buzz noise, due to composite construction.
- 6. 100% Lead(Pb)-Free and RoHS compliant.
- 7. Operating temperature -40~+125 $^{\circ}$ C (Including self temperature rise)

## 2. Applications

- 1. DC/DC converters in distributed power systems.
- 2. DC/DC converter for Field Programmable Gate Array(FPGA).
- 3. Battery powered devices.
- 4. Thin type on-board power supply module for exchanger.
- 5. VRM for server.
- 6. High current, low profile POL converters.
- 7. PDA/notebook/desktop/server and battery powered devices.

### 3. Dimensions

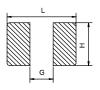






Series	Α	A`	В	С	D	E
BSMA1770	17.8±0.5	16.9±0.3	16.9±0.3	6.7±0.3	2.3±0.3	11.9±0.3

#### **Recommend PC Board Pattern**



L(mm)	G(mm)	H(mm)
18.5	12.0	12.5

Note: 1.PCB layout is referred to standard IPC-7351B
2. The above PCB layout reference only.

- 3. Recommend solder paste thickness at 0.17mm and above.

# 4. Part Numbering



A: Series

B: Dimension BxC. C: Type Standard. D: Inductance 820=82.0uH. E: Inductance Tolerance M=±20%.

# 5. Specification

Part Number	Inductance L0 A(uH) ±20%	Curre	Rating int DC s ( A )	Curre	ration ent DC ( A )	DCR (mΩ)	DCR (mΩ) Max
	120%	Тур	Max	Тур	Max	Тур	IVIAX
BSMA1770SP-R47M	0.47	60	55	110	100	0.7	0.9
BSMA1770SP-R56M	0.56	56	50	80	70	0.81	0.97
BSMA1770SP-1R0M	1.00	46	42	50	45	1.06	1.3
BSMA1770SP-1R5M	1.50	39	35	46	40	1.5	1.8
BSMA1770SP-1R8M	1.80	35	32	40	34	1.7	2.0
BSMA1770SP-2R2M	2.20	32	30	35	32	1.8	2.2
BSMA1770SP-3R3M	3.30	30	28	32	29	2.7	3.3
BSMA1770SP-4R7M	4.70	28	26	29	26	3.7	4.5
BSMA1770SP-6R8M	6.80	24	22	25	22	6.0	7.2
BSMA1770SP-100M	10.0	21	19	22	19	9.2	10.6
BSMA1770SP-150M	15.0	16	14	16	14	12.8	15.5
BSMA1770SP-220M	22.0	13.5	11.5	13.5	11.5	20.5	24
BSMA1770SP-330M	33.0	12	10	12	10	32	37
BSMA1770SP-470M	47.0	9.5	8.0	9.5	8.0	40	47
BSMA1770SP-680M	68.0	8.0	6.5	8.5	7.2	66	76
BSMA1770SP-820M	82.0	6.5	5.7	8.0	6.5	69	83

#### Note:

- 1. Test frequency : Ls : 100KHz /1.0V.
- 2. All test data referenced to  $25\,^\circ\!\mathbb{C}$  ambient.
- $\textbf{3. Testing Instrument} (or\ equ): A \textit{gilent } 4284A, \textit{E4991A}, 4339B, \textit{KEYSIGHT E4980A/AL}, chroma 3302, 3250, 16502.$
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately  $\,^\Delta T$  of 40  $^\circ\! C$
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- $\ensuremath{\mathbf{7.}}\ \ \mbox{Special inquiries besides the above common used types can be met on your requirement.}$
- 8. Irms Testing: Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components.

  Therefore temperature rise should be verified in application conditions.
- 9. Rated DC Current : The less value whith is Irms or Isat

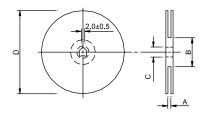
### 6. Material List

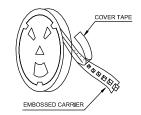


NO	Items	Materials	
1	Core	Alloy Powder .	
2	Wire	Polyester Wire or equivalent.	
3	Clip	100% Pb free solder(Ni+SnPlating)	
4	lnk	Halogen-free ketone	

# 7. Packaging Information

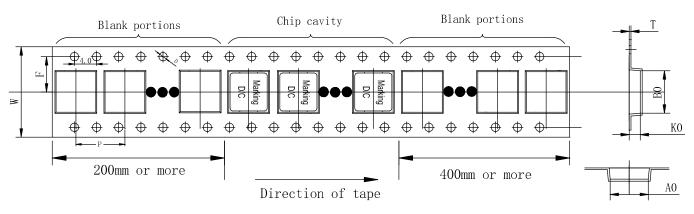
### (1) Reel Dimension





Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x32mm	32.4+2/-0	100±2	13+0.5/-0.2	330

#### (2) Tape Dimension

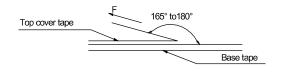


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
BSMA	1770	18.5±0.1	17.5±0.1	7.5±0.1	24±0.1	32±0.3	14.2±0.1	0.5±0.05	1.5±0.1

#### (3) Packaging Quantity

BSMA	1770
Chip / Reel	200

### (4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Tearing Speed	Room Temp.	Room Humidity	Room atm
mm	(℃)	(%)	(hPa)
300±10%	5~35	45~85	860~1060

# 8. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	110~+40°C,50~60%RH (Product without taping) 240~+125°C(on board)	
Electrical Performance	Test	
Inductance	Perfect to standard electrical characteristics list	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop $\triangle$ L(%)
Heat Rated Current (Irms)	Approximately △T40℃	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃).  1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 3times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Temperature: 125±2°C (Inductor, ambient + temp rise) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs. Preconditioning: Run through IR reflow for 3times.
Load Humidity		(IPC/JEDECJ-STD-020E Classification Reflow Profiles) Humidity: 85±2% R.H, Temperature: 85℃±2℃ Duration: 1000hrs Min. Bead:with 100% rated current, Inductance: with 100% rated current Measured at room temperature after placing for 24±2 hrs.
Moisture Resistance	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) 1. Baked at50 °C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm2^\circ\mathbb{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^\circ\mathbb{C}$ in 2.5hrs. 3. Raise temperature to $65\pm2^\circ\mathbb{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^\circ\mathbb{C}$ in 2.5hrs,keep at $25^\circ\mathbb{C}$ for 2 hrs then keep at -10° for 3 hrs 4. Keep at $25^\circ\mathbb{C}$ 80-100%RH for 15min and vibrate at the frequency of 10 to $55$ Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 3 times. ( IPC/JEDEC/J-STD-020E Classification Reflow Profiles) Condition for 1 cycle Step1: -40±2°C 30±5min Step2: 125±2°C ≤0.5min Step3: 125±2°C 30±5minNumber of cycles: 500 Measured at room femprature after placing for 24±2 hrs.
Vibration		Preconditioning: Run through IR reflow for 3 times.  ( IPC/JEDECJ-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz ~2KHz ~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude: 10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations)

### **BULL WILL**

Item	Performance	Test Condition			
Bending	Appearance: No damage.	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.			
Shock	Inductance: within $\pm$ 10% of initial value  Q: Shall not exceed the specification value.  RDC: within $\pm$ 15% of initial value and shall not exceed the specification value	Type Peak Normal duration (D) Wave form (Vi)ft/sec			
		SMD 50 11 Half-sine 11.3			
		Lead 50 11 Half-sine 11.3			
Solderability	More than 95% of the terminal electrode should be covered with solder	a. Method B, 4 hrs @155°C dry heat @235°C±5°C Test time:5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C Test time: 30 +0/-0.5 seconds.			
Resistance to Soldering Heat		Depth: completely cover the termination  Temperature (°C) Time(s) ramp/immersion and emersion rate heat cycles  260 ±5			
		(solder temp)   10 ±1   25mm/s ±6 mm/s   1    Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC			
Terminal Strength	Appearance: No damage. Inductance: within $\pm$ 10% of initial value Q: Shall not exceed the specification value. RDC: within $\pm$ 15% of initial value and shall not exceed the specification value e	J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, applyaforce(>0805inch(2012mm):18g,<=0805inch(2012mm):0.5kg) to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.			
		Substrate Dress tool			

Note: When there are questions concerning measurement result: measurement shall be made after  $48 \pm 2$  hours of recovery under the standard condition.

### 9. Soldering Specifications

#### (1) Soldering

Mildly activated rosin fluxes are preferred. BULL WILL terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### (2) IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

#### (3) Iron Reflow:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)

- Preheat circuit and products to 150°C Never contact the ceramic with the iron tip
- 355℃ tip temperature (max) 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1 0mm
- Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

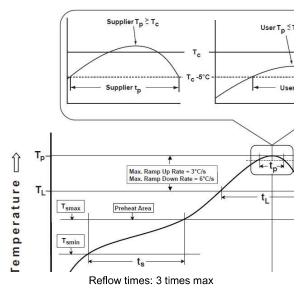
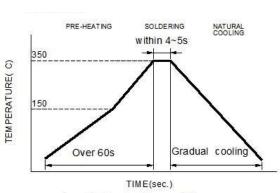


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max.

Soldering iron Method : 350± 5°C max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly	
Preheat -Temperature Min(T <sub>smin</sub> ) -Temperature Max(T <sub>smax</sub> ) -Time(t <sub>s</sub> )from(T <sub>smin</sub> to T <sub>smax</sub> )	150℃ 200℃ 60-120seconds	
Ramp-up rate(T <sub>L</sub> to T <sub>p</sub> )	3°C/second max.	
Liquidus temperature(T <sub>L</sub> ) Time(t <sub>L</sub> )maintained above T <sub>L</sub>	217°C 60-150 seconds	
Classification temperature(T <sub>c</sub> )	See Table (1.2)	
Time( $t_p$ ) at Tc- $5^{\circ}\mathbb{C}$ (Tp should be equal to or less than Tc.)	*< 30 seconds	
Ramp-down rate(Tp to TL)	6℃ /second max.	
Time 25℃ to peak temperature	8 minutes max.	

 $\textbf{\textit{Tp}}: \mbox{maximum peak package body temperature, } \textbf{\textit{Tc}}: \mbox{the classification temperature.}$ 

For user (customer) **Tp** should be equal to or less than **Tc.** 

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm³ >2000
PB-Free Assembly	<1.6mm	260℃	260℃	260℃
	1.6-2.5mm	260℃	250℃	245℃
	≥2.5mm	250℃	<b>245</b> ℃	245℃

Reflow is referred to standard IPC/JEDEC J-STD-020E.

<sup>\*</sup> Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

#### 10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48  $\pm$  2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearne.
- (9) The high power ultrasonic washing may damage the choke body.

#### **Application Notice**

- · Storage Conditions
  - To maintain the solderability of terminal electrodes:
  - 1. BULL WILL products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

# 11. Typical Performance Curves

5.0 IDC(A) 7.5

